

Office of Finance  
Commissioner for Patents  
Washington, DC 20231

# 31 Reg for  
Office of Finance  
Refund Branch

April 2, 2003 06-10-23

2003 APR -9 PM 4:14

US PATENT & TRADEMARK  
OFFICE

To Whom it May Concern:

I am an independent inventor with a number of pending *pro se* applications at the US PTO. Recently, I received my first notice of allowance and issuance for one of my applications. In the notice, the examiner indicated that I had over-paid, as I included a fee payment of \$180.00 to cover the Information Disclosure Statement which I co-filed with the original application. Prior to the notice I was not aware that the \$180.00 was applicable to IDS filings after initial application filings, so I similarly overpaid on a number of other applications as well.

I would like to formally request a refund of the \$180 for the allowed application entitled "Parallel And Shared Parallel Analog-to-Digital Conversion for Digital Imaging", which has application number 10/073,385, a filing date of 2/12/2002, and which was examined by Howard L. Williams. The most convenient method for a refund would be a check mailed to me at my address listed on the next page of this letter.

If possible I would also like to request refunds of \$180 IDS fees on the same grounds (IDS statements co-filed with original application) for the following applications that are still awaiting a first action:

09/928,897                      08/14/01  
Symbol Constellations Having Second-Order Statistics with Cyclostationary Phase

09/963,623                      09/27/01  
Non-Constant Reduced-Complexity Multiplication in Signal Processing Transforms

09/976,920                      10/15/01  
Shared Multiplication in Signal Processing Transforms

10/072,286                      02/11/02  
Digital Images with Composite Exposure

10/084,803                      02/28/02  
Shared Parallel Digital-to-Analog Conversion

10/104,534                      03/25/02  
Low-Cost Multiplication in Small DFT Butterflies

10/118,635            04/10/02  
Shared Multiplication for Constant and Adaptive Digital Filters

10/122,654            04/15/02  
Multiple-Output Counters for Analog-to-Digital and Digital-to-Analog Conversion

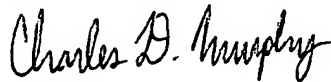
10/159,936            06/03/02  
Low-Complexity High-Speed Analog-to-Digital Converters

10/179,937            06/24/02  
Analog-to-Digital Conversion with Non-Linear Reference Waveforms

10/228,372            08/27/02  
Non-Linear Reference Waveform Generators for Data Conversion and Other Applications

Altogether, I incorrectly submitted \$180 fee payments for IDS filings included with initial 12 separate applications. I hope I will be able to get these payments refunded shortly.

Sincerely,



Charles D. Murphy

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